[Total No. of Questions: 8]

[Total No. of Printed Pages :2]
Enroll No.

102

EC-102

M.Tech. (DC)–I Sem (Reg./Ex.)
Examination, March.-2021
Data Communication and Computer Networks
_Time: Three Hours

Maximum Marks:70

Note: Attempt any five questions. (Each question carries equal marks)

- Discuss about vertical and longitudinal redundancy check code and its error detecting capability.
 - (b) Compare and analyze the performance of different ARQ techniques of error control.
- Q.2 What is framing in data link layer? Explain the different method of framing in data link layer.
- Q.3 (a) Explain the cyclic redundancy check with taking a suitable example.
 - (b) Discuss about RS-232C and X.21 standard.
- Q.4 Discuss various ARQ technique for error control and their comparison and performance analysis.
- Q.5 (a) State and explain Bellman Ford algorithm.
 - (b) What is an error? Explain the types of errors.

Q.6 (a) Explain the bus type topology and ring topology networks. Compare their performance.

- (b) Discuss various IEEE standard for LAN.
- Q.7 (a) What is deadlock? Discuss some deadlock avoidance technique.
 - (b) Explain ATM and frame relay.
- Q.8 Write a short note (any three):
 - (a) TCP/IP protocol
 - (b) USB LAN
 - (c) Congestion control
 - (d) Parity check
